

**IN THE ABSTRACT:**

The Abstract as amended below with a replacement Abstract shows added text with underlining and deleted text with ~~strikethrough~~.

An error flag generation apparatus and method for error correction, wherein the apparatus includes: a frame-sync error memory which stores frame-sync error information for each data block; a BIS (Burst Indicator Subcode) error flag memory which stores a BIS error flag for each data block; and an error flag generator, which generates an error flag indicating error existence/absence for ECC (Error-Correction Coding) data with reference to the frame-sync error information stored in the frame-sync error memory and the BIS error flag stored in the BIS error flag memory. ~~Therefore, the error flag generation apparatus and method for error correction can be easily implemented with improved error correction performance and be advantageous in cost.~~